

<b>Notice of References Cited</b>	Application/Control No. 09/980,754	Applicant(s)/Patent Under Reexamination LERCH ET AL.	
	Examiner Julio J. Maldonado	Art Unit 2823	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-5,913,149	06-1999	Thakur et al.	438/762
*	B	US-6,087,229	07-2000	Aronowitz et al.	438/287
*	C	US-6,218,720 B1	04-2001	Gardner et al.	257/510
*	D	US-6,274,510 B1	08-2001	Wilk et al.	438/763
*	E	US-5,714,413	02-1998	Brigham et al.	438/301
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf et al., Silicon processing for the VLSI Era, Volume 1: Process Technology, 1986 Lattice Press, pages 264-265.
	V	
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.